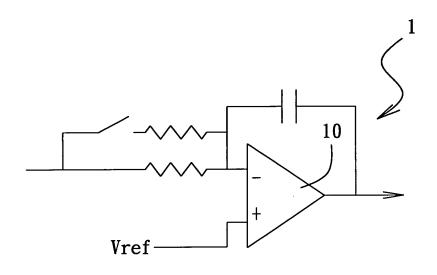
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants	Tsin-Yuan Chang, et al.
Serial No Pending	December 30, 2003
Title of Application:	Method And Apparatus For Controlling A Dual-Slope Integrator Circuit To Eliminate Settling Time Effect

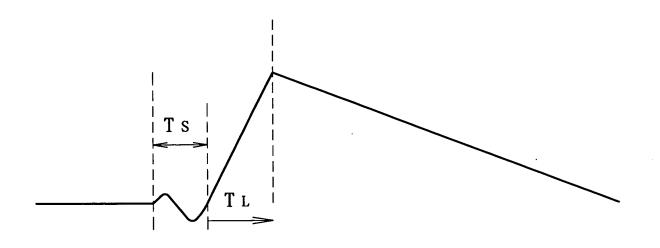
Mail Stop Patent Application Commissioner for Patents Post Office Box 1450 Alexandria, VA 22313-1450

Cover Sheet For Four Sheets Of Drawings

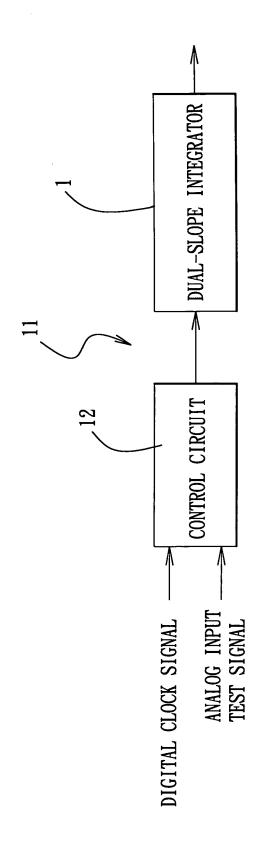
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F I G. 1 PRIOR ART



F I G. 2 PRIOR ART



F I G. 3 PRIOR ART

